

# Dr Jami Sridevi

**Areas of Interest:** Power Systems, Control Systems, Smart Grid, Deregulation, FACTS

## Education

- Ph.D., JNTU, Hyderabad
- ME, Andhra University Visakhapatnam
- BE, Gandhi Institute of Technology and Management ( GITAM ), Andhra University, Visakhapatnam

## Academic/Industrial Experience

1. Professor, EEE, Gokaraju Rangaraju Institute of Engineering And Technology, Hyderabad, 2014 - Till date
2. Associate Professor, EEE, Gokaraju Rangaraju Institute of Engineering And Technology, Hyderabad, 2009 - 14
3. Senior Assistant Professor, EEE, Gokaraju Rangaraju Institute of Engineering And Technology, Hyderabad, 2007 - 09
4. Assistant Professor, EEE, Guru Nanak Engineering College, Hyderabad, 2006 – 07
5. Assistant Professor, EEE, Bharath Institute of Engineering And Technology, Hyderabad, 2005 – 06
6. Assistant Professor, EEE, P.V.P.Siddhartha Institute of Technology, Vijayawada, 2003 – 05

## R & D Projects

NIL

## Selected Publications

[1] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Congestion Management and ATC Enhancement with optimal location of FACTS devices using Sensitivity indices", International Review on Modelling and Simulations (I.RE.MO.S), **Impact Factor 6.55**, ISSN: 1974-983X, Vol.4, N.4, August 2011.

[2] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Influence of FACTS Devices on Congestion Management in Deregulated Power System", ICGST-ACSE Journal, ISSN: 1687-4811, Volume 11, Issue 2, November 2011.

[3] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Comparison of Voltage Stability by optimal location of TCSC and SVC using Loss Sensitivity Method under Contingencies", International Review on Modelling and Simulations (I.RE.MO.S), **Impact Factor 6.55**, ISSN: 1974-983X, Vol.4, N.4, October 2011.

[4] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Impact of FACTS devices on Zonal Congestion Management in Deregulated Power System", Innovative Systems Design and Engineering, IISTE, ISSN: 2222-2871, **IC Impact factor value 6.94**, Vol 3, No 1, 2012.

[5] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Optimal Location of FACTS Devices for Zonal Congestion Management under Contingency", International Journal of Applied Engineering Research, ISSN 0973-4562, Impact factor 1.07, Volume 7, Number 6 (2012) pp. 599-611.

[6] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Role of FACTS Devices on Zonal Congestion Management Ensuring Voltage Stability under Contingency", International Journal of Advances in Engineering & Technology, ISSN: 2231-1963, Impact Factor 1.07, Vol. 3, Issue 2, pp. 635-641, May 2012.

[7] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Zonal Power Quality improvement using Static Var Compensator for an Indian Utility System", International Journal of Engineering Research and Applications (IJERA) , Impact Factor 0.493, ISSN: 2248-9622, Vol. 2, Issue 3, May-Jun 2012, pp. 1320-1325.

[8] **J. Sridevi**, "A New Active Power Factor Correction Controller using Boost Converter", International Science Congress Association, Research Journal of Engineering, ISSN: 2278- 9472, Vol. 2(8), pp. 7-11, August 2013.

[9] **J. Sridevi**, "Implementation Of Thyristor Controlled Series Capacitor (TCSC) In Transmission Line Model Using Arduino", Int. Journal of Engineering Research and Applications, ISSN: 2248-9622, Vol. 4, Issue 9( Version 5), September 2014.

[10] **J.Sridevi, G.Sravanthi**, "Optimal placement and sizing of DG based on Novel Index and PSO method for Minimization of Losses", International Journal of Scientific & Engineering Research, ISSN 2229-5518, Volume 7, Issue 6, pp. 510-514, June 2016.

[11] V.Usha Rani, **Dr.J.Sridevi**, "Transmission Line Protection for Symmetrical and Unsymmetrical Faults using Distance Relays", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 5, Issue 10, pp. 8170-8178, October 2016.

[12] V Usha Rani , **J Sridevi**, " Loss Minimization and Voltage Profile Improvement with Network Reconfiguration and Distributed Generation ", International Journal of Computer Engineering In Research Trends, ISSN: 2349-7084, Volume 4, Issue 10, October - 2017, pp. 449-455

[13] K. Teja, **Dr.J.Sridevi** , "Three Phase Fault Analysis With Auto Reset On Temporary Fault And Permanent Trip Otherwise", International Journal of Innovative Research and Advanced Studies (IJIRAS) Volume 4 Issue 10, ISSN: 2394-4404, October 2017

[14] Azra Zaineb, **J. Sridevi**, "Novel Method for Loss Reduction and Voltage Profile Improvement with Multiple DGs", International Journal of Scientific & Engineering Research Volume 8, Issue 11, ISSN 2229-5518, November-2017.

[15] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Assessment of Available Transfer Capability using AC Power Transfer Distribution Factors", Proceeding of National Conference on Power System Planning , operation & optimization(PSPOO), J.N.T.University, Kakinada.

[16] **J. Sridevi**, M. Abhiram, Dr. J. Amarnath, Dr. G. Govinda Rao, "ATC Enhancement with Multi type FACTS devices", Proceeding of National Conference on Power System Planning , operation & optimization(PSPOO), J.N.T.University, Kakinada.

[17] **J. Sridevi**, Dr. J. Amarnath, Dr. G. Govinda Rao, "Zonal Congestion Management with Optimal Placement of FACTS Devices under Contingency", Proceeding of National Conference on Recent Trends in Power systems and Power Electronics(NCPSPE-2012), BVC Engineering College, Odalarevu.

### **Workshops Conducted/Attended**

1. A Two day workshop on "Electrical Machines" organized by Guru Nanak Engineering College from 7<sup>th</sup> September to 9<sup>th</sup> September 2006.
2. A Two day workshop on "Faculty Development Program" organized by GRIET from 6<sup>th</sup> December to 7<sup>th</sup> December 2007.
3. One day tutorial on "Virtual High Voltage Laboratory" conducted by JNTU, Hyderabad on 28<sup>th</sup> August 2010.
4. A National level workshop conducted by IIT BOMBAY organized by GRIET Hyderabad on "Introduction to Research Methodologies" from June 25<sup>th</sup> to 4<sup>th</sup> July 2012.
5. A Two day ISTE workshop conducted by IIT BOMBAY organized at GRIET, Hyderabad on "AAKASH for Education", 10<sup>th</sup>, 11<sup>th</sup> November, 2012.
6. One day workshop on "SMART GRID TECHNOLOGY" on 9<sup>th</sup> Feb 2013 conducted by EEE dept GRIET, Hyderabad.
7. Two days workshop on 'Model based Embedded System Design using VisSim', 7<sup>th</sup> and 8<sup>th</sup> March, 2013 at Gokaraju Rangaraju Institute Engineering and Technology conducted by Trident Techlabs in association with Visual Solution Incorporated.
8. Two-week ISTE Workshop On 'Analog Electronics' , National Mission on Education through ICT (MHRD, Govt. of India), June 4<sup>th</sup>, 2013 to June 14<sup>th</sup>, 2013, under the eOUTREACH programme name "Empowerment of Students & Teachers through Synchronous & Asynchronous Instruction," IIT Kharagpur attended at Remote Center GRIET, Hyderabad.
9. Seminar on "MATLAB and Simulink for Engineering Education", MathWorks, 26<sup>th</sup> July, 2013, Hyderabad.
10. Seminar on "Engineering & Science Innovation Summit 2013", National Instruments, 13<sup>th</sup> September, 2013, Hyderabad
11. Two-week Workshop On 'Signals & Systems' conducted by IIT BOMBAY through National Mission on Education through ICT (MHRD, Govt. of India), organized at GRIET from Jan 2<sup>nd</sup>, 2014 to Jan 12<sup>th</sup>, 2014.
12. One week Faculty Development Program on "Soft Computing & Machine Learning", from 3<sup>rd</sup> March 2014 to 8<sup>th</sup> March 2014 at GRIET.

13. Two-week Workshop On 'Fluid Mechanics' conducted by IIT Kharagpur through National Mission on Education through ICT (MHRD, Govt. of India), organized at GRIET from May 20<sup>th</sup>, 2014 to May 30<sup>th</sup>, 2014.
14. One-week Workshop on 'Computer Networking' conducted by IIT Kharagpur through National Mission on Education through ICT (MHRD, Govt. of India), organized at GRIET from June 30<sup>th</sup>, 2014 to July 6<sup>th</sup>, 2014.
15. Two day workshop on "Teacher Effectiveness" conducted by IIT Hyderabad under TEQIP during December 5-6, 2014.
16. Two day workshop on "Advanced Softwares for Power System Simulation" conducted by NIT Warangal during 21<sup>st</sup> and 22<sup>nd</sup> February, 2015.
17. Three day workshop on " Computer Applications to Power Systems using Mi-Power",organized by JNTUH college of Engineering, Hyderabad during 18-20 March 2015.
18. One week workshop on "Wireless sensor networks and its applications", conducted by JNTUH College of Engineering, Hyderabad during May 4<sup>th</sup> to May 9<sup>th</sup>, 2015.
19. One week workshop on "Guide to a Passionate Teacher", organized at GRIET, Hyderabad during June 29<sup>th</sup> –July 4<sup>th</sup>, 2015.
20. One week workshop on "Management Capacity Enhancement programme for Administrators", conducted by Indian Institute of Management Lucknow during August 24-28, 2015.
21. One week Training Programme on " Power Electronics & Drives for Vehicular Systems Technology Status & Future Trends", organized by Osmania University, Hyderabad during November 23-28, 2015.
22. One week Course of NI labVIEW Core 1 and Core 2 organised by GRIET during 4-8 January 2016.
23. One week National level Faculty Development Program on " Solar PV Grid Connected Power Plant", organized by BVRIT Hyderabad College of Engineering for women during 5-10 December 2016.
24. Two-week ISTE Workshop On 'Electric Power System' , National Mission on Education through ICT (MHRD, Govt. of India), June 12<sup>th</sup>, 2017 to July 15<sup>th</sup>, 2017, organized by IIT Kharagpur attended at Remote Center GRIET, Hyderabad.

#### **Guest Lectures National/International**

- Visited universities like IIT Hyderabad, IIM Lucknow.
- Received Honor Code International certificate in Discrete Time Signals and Systems from Rice University with A Grade through edx.
- Received Honor Code International certificate in Principles of Electric Circuits from Tsinghua University with A Grade through edx.
- Invited as Guest Speaker On "Computer Methods in Power Systems" on 13-02-2015 at Lords Institute of Engineering and Technology.
- A Project titled "Design and Hardware Implementation of Five phase Induction Motor with Five phase Supply" is recommended from DST under Young Scientist Award in the year 2015.
- Received best teacher award in GRIET in 2008.
- Achieved 100% results in Linear system analysis (2008), HVDC Transmission (2012), Microprocessors and Microcontrollers (2013).
- Editorial Board Member and a valued reviewer for International Journal of Modern Trends In Engineering and Research.
- Served as technical chairperson for Pragnya'16 at JNTUH college of Engineering, Hyderabad on March 19, 2016